

124	255
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER:

U.S. UTILITY Patent Application

m.l O.I.P.E. SCANNED <u>TD4</u> Q.A. <u>2004</u>	PATENT DATE
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APPLICATION NO. 09/826512	CONT/PRIOR F	CLASS 35 174	SUBCLASS 255	ART UNIT 2541 2537	EXAMINER ALCALA
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NTS Michioy Horuchi
Takashi Kurihara

Wiring substrate, method of manufacturing the same and semiconductor device

PTO-2040
12/99

ISSUING CLASSIFICATION									
ORIGINAL			CROSS REFERENCE(S)						
CLASS		SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
174		255	174	256	260				
INTERNATIONAL CLASSIFICATION			361	768	771	779			
#05K		1/03							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 7	Figs. Drwg. 20	Print Fig. 7	Total Claims 18	Print Claim for O.G. 1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)			ISSUE FEE	
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